## Notice of References Cited

Application/Control No. 10/664,055	Applicant(s)/Patent Under Reexamination BARRETT ET AL.		
Examiner	Art Unit		
Aimee J. Li	2183	Page 1 of 1	

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-4,498,136	02-1985	Sproul, III, William W.	710/262
*	В	US-4,777,587	10-1988	Case et al.	712/235
*	С	US-4,755,935	07-1988	Davis et al.	712/233
*	D	US-5,557,759	09-1996	Crump et al.	709/219
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	ı	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	ρ					
	R					
	S					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Bigelow, Ken. "The Two-Input Multiplexer", "The 2-to-4 Line Decoder/Demultiplexer", and "The 1-to-2 Line Decoder/Demultiplexer". ©1994, 2000-2006.
	>	Heuring, Vincent P. and Jordan, Harry F. "Computer Systems Design and Architecture". Reading, Mass.: Addison Wesley Longman, Inc., ©1997. Pages 497-499.
	w	
	x	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.